

Amendments to the Specification

Please replace paragraph [0024] with the following amended paragraph:

[0024] Figure 4 is the preferred implantation of the CSF system in the patient; and

Please replace paragraph [0025] with the following amended paragraph:

[0025] Figure 5 is a detailed view of the CSF Controller; and [[.]]

Please add the following new paragraph after paragraph [0025]:

[0025.1] Figure 6 is a schematic illustration of the microprocessor based subsystem of the CSF Controller.

Please replace paragraph [0038] with the following amended paragraph:

[0038] In addition to the elements described above, which are part of the flow paths, there is a microprocessor-based subsystem internal to the CSF Controller, as shown in Figure 6. This subsystem 600 preferably comprises a microprocessor 610, its associated memory 620, a Real Time Clock 630, a wireless transceiver 640, a piezo electric buzzer 650 and other essential electronics. An internal

battery 660 powers this subsystem. The microprocessor 610 is responsible for monitoring and controlling many of the operations enumerated above, such as monitoring the inclination sensor, adjusting the check valve cracking pressure, and monitoring the pressure sensor. The microprocessor is also capable of receiving commands and returning status to the external programmer 670 via the wireless transceiver 640. The memory 620 is used to store data requested by the external programmer 670, such as pressure readings, inclination angle, and time. This data can be transmitted back to the external programmer 670 as requested. The Real Time Clock 630 is used to enable the Controller to perform certain diagnostics at specific times. The piezo electric buzzer 650 is used to alert the patient of certain conditions. As an example, the buzzer 650 may sound once per hour to indicate a low battery condition. This method of warning is used currently by those skilled in the art for a variety of device, such as pacemakers.